

FIG. 1

2/13

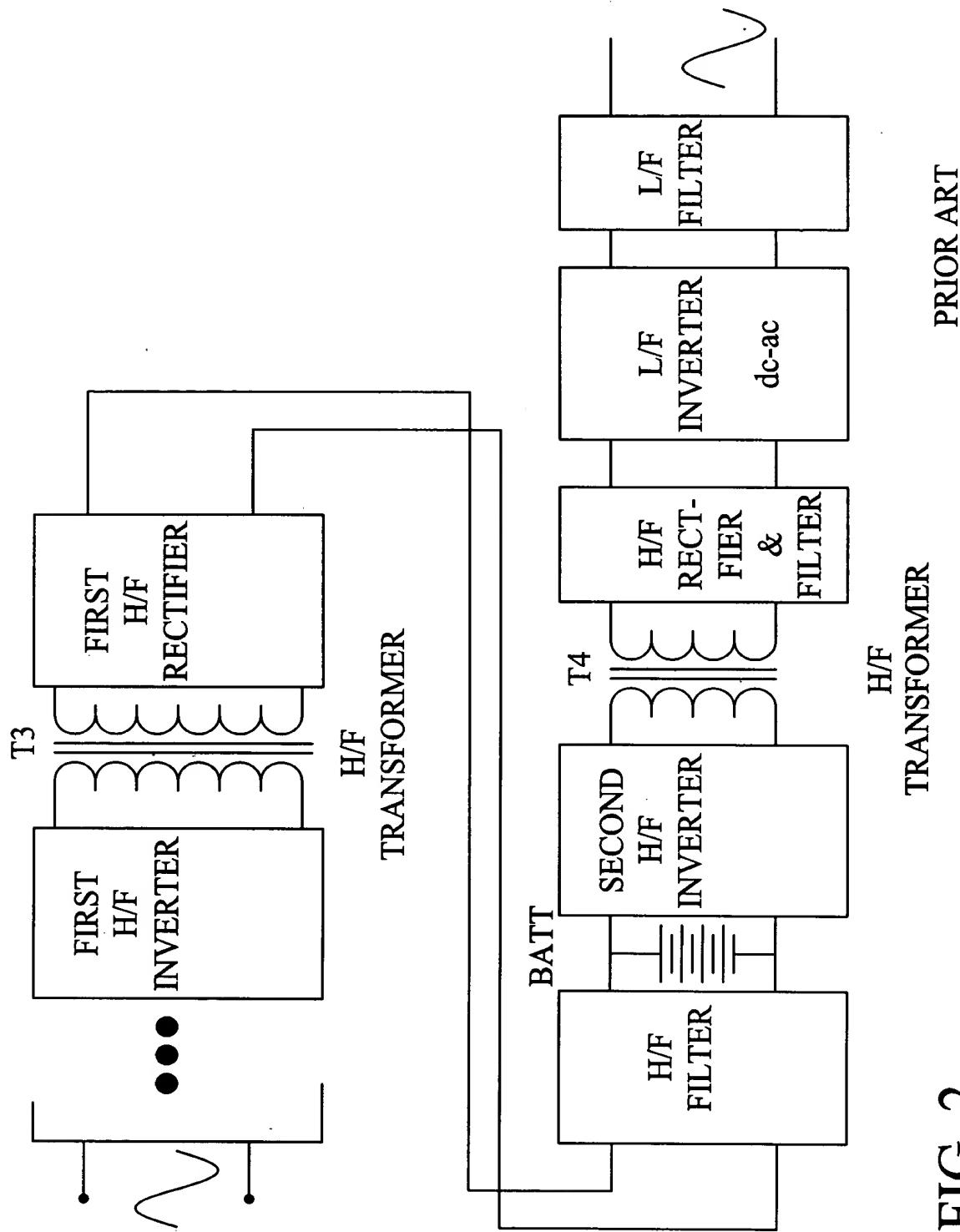


FIG. 2

3/13

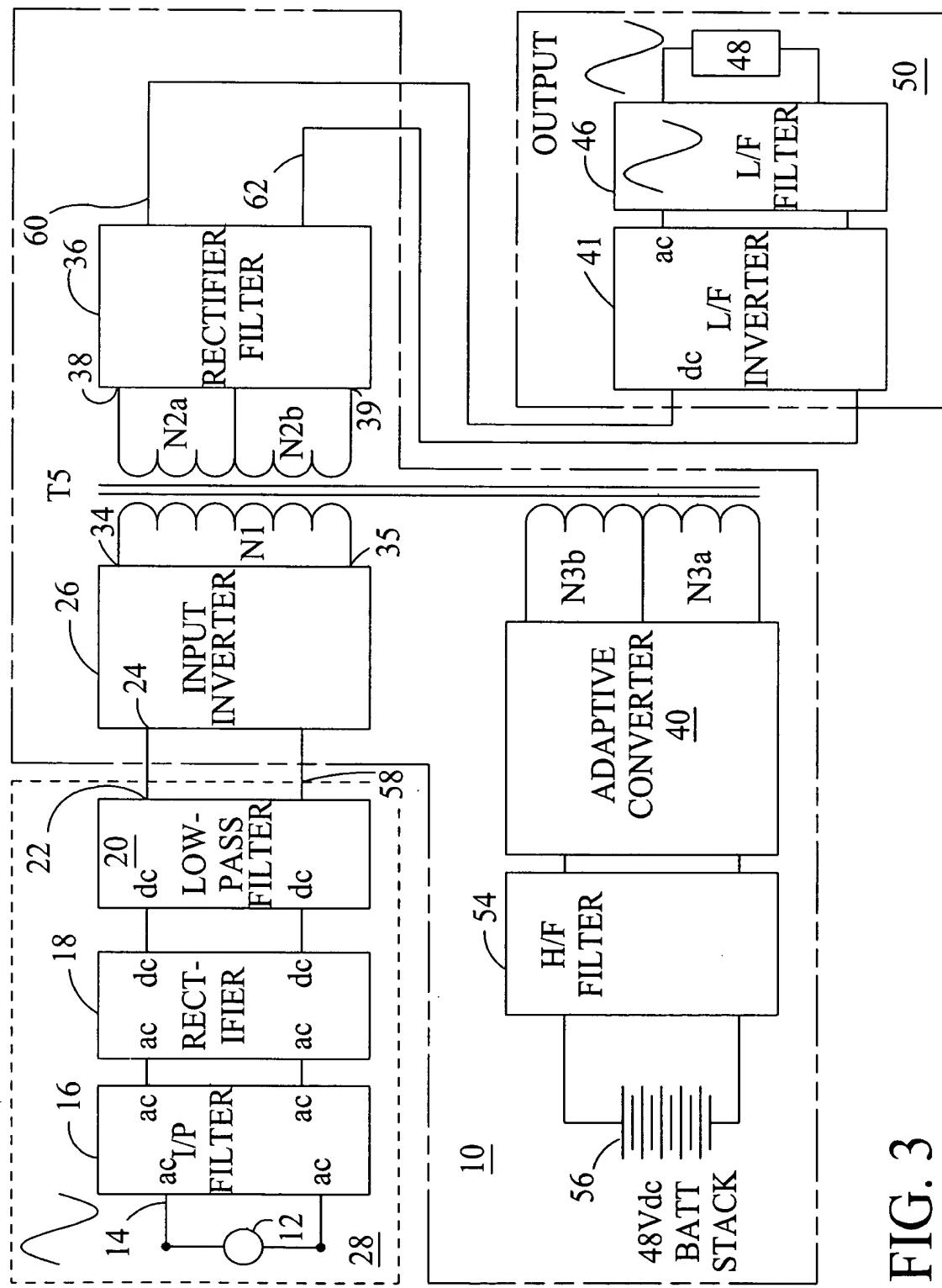


FIG. 3

4/13

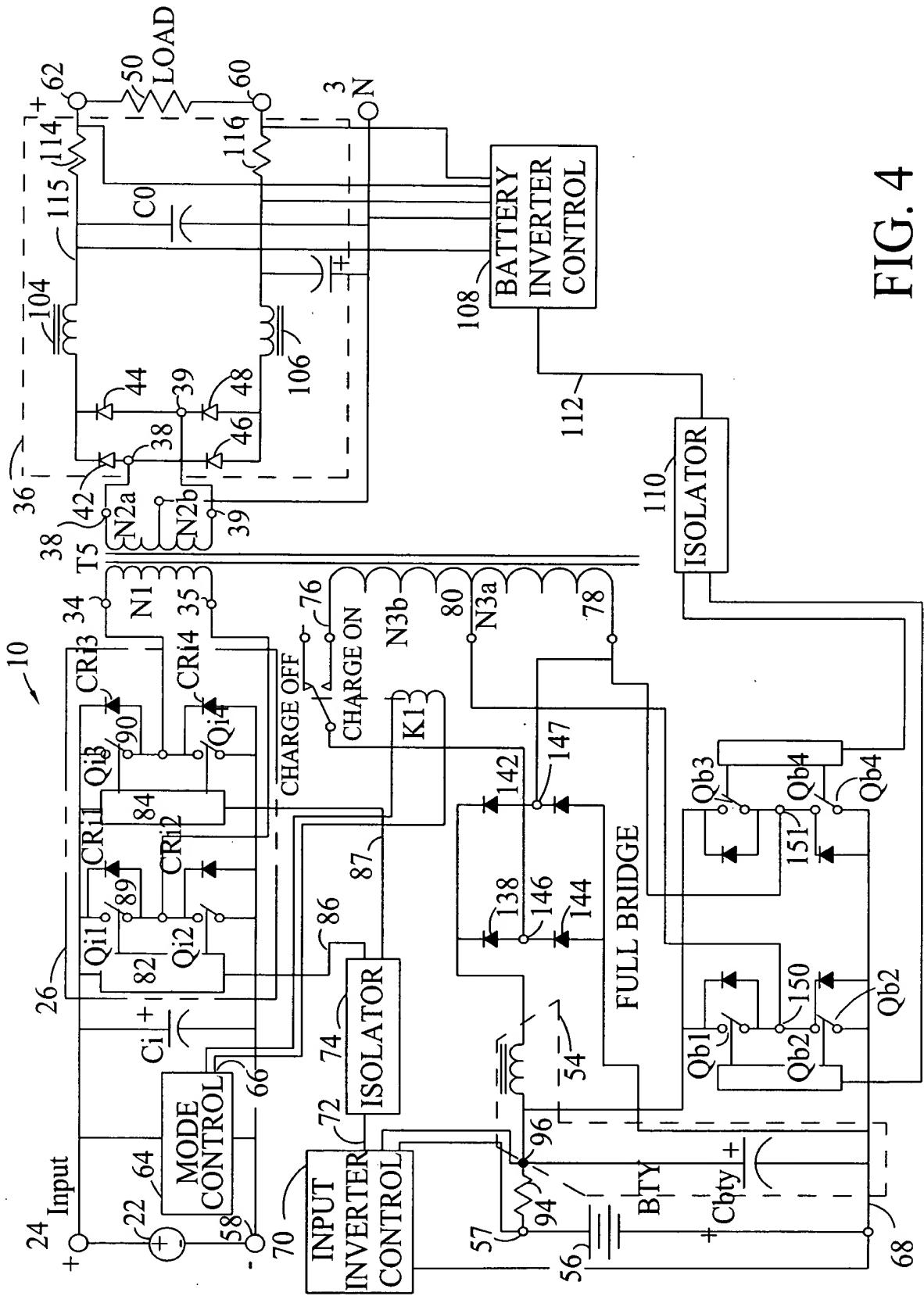


FIG. 4

5/13

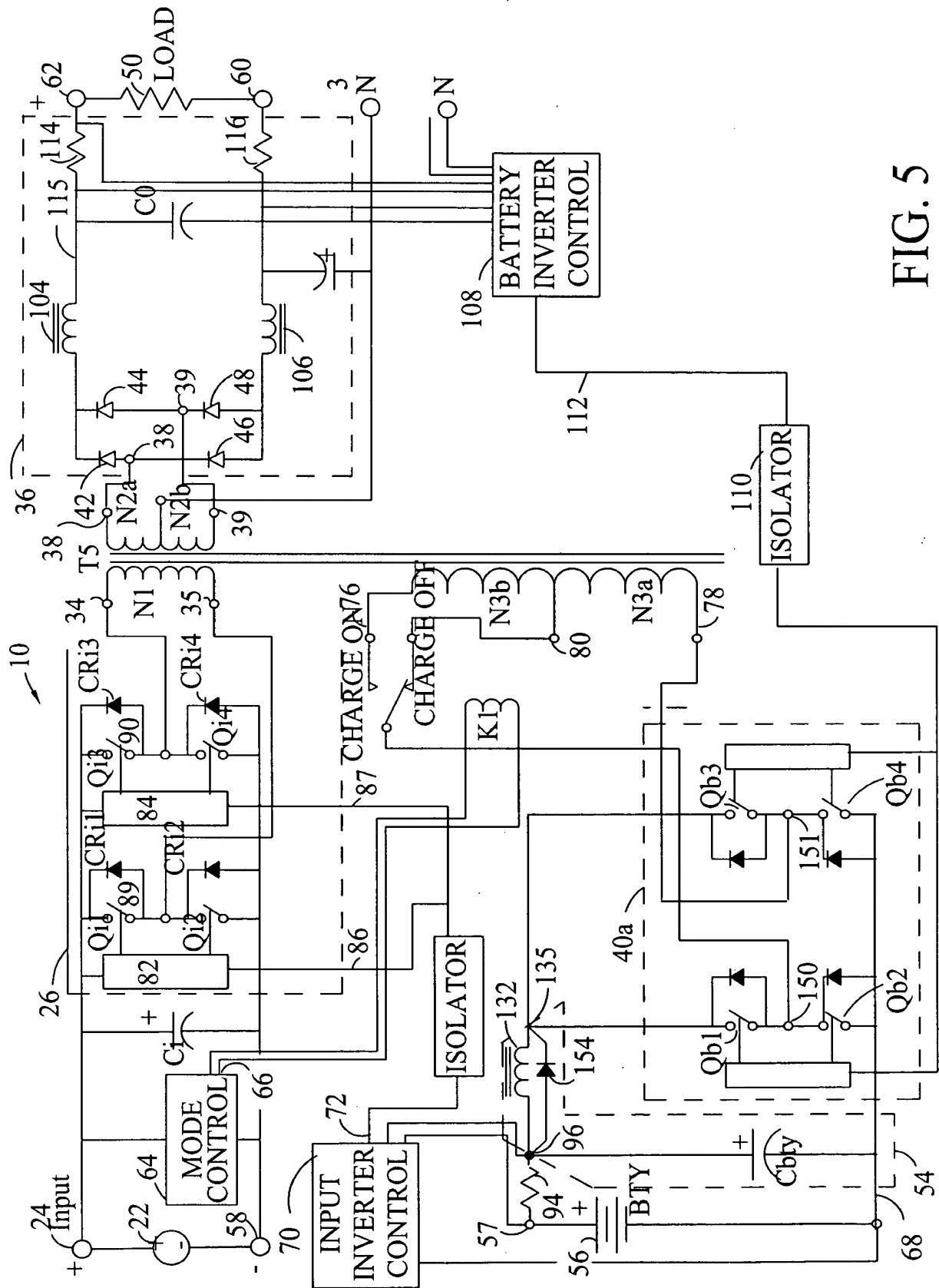


FIG. 5

6/13

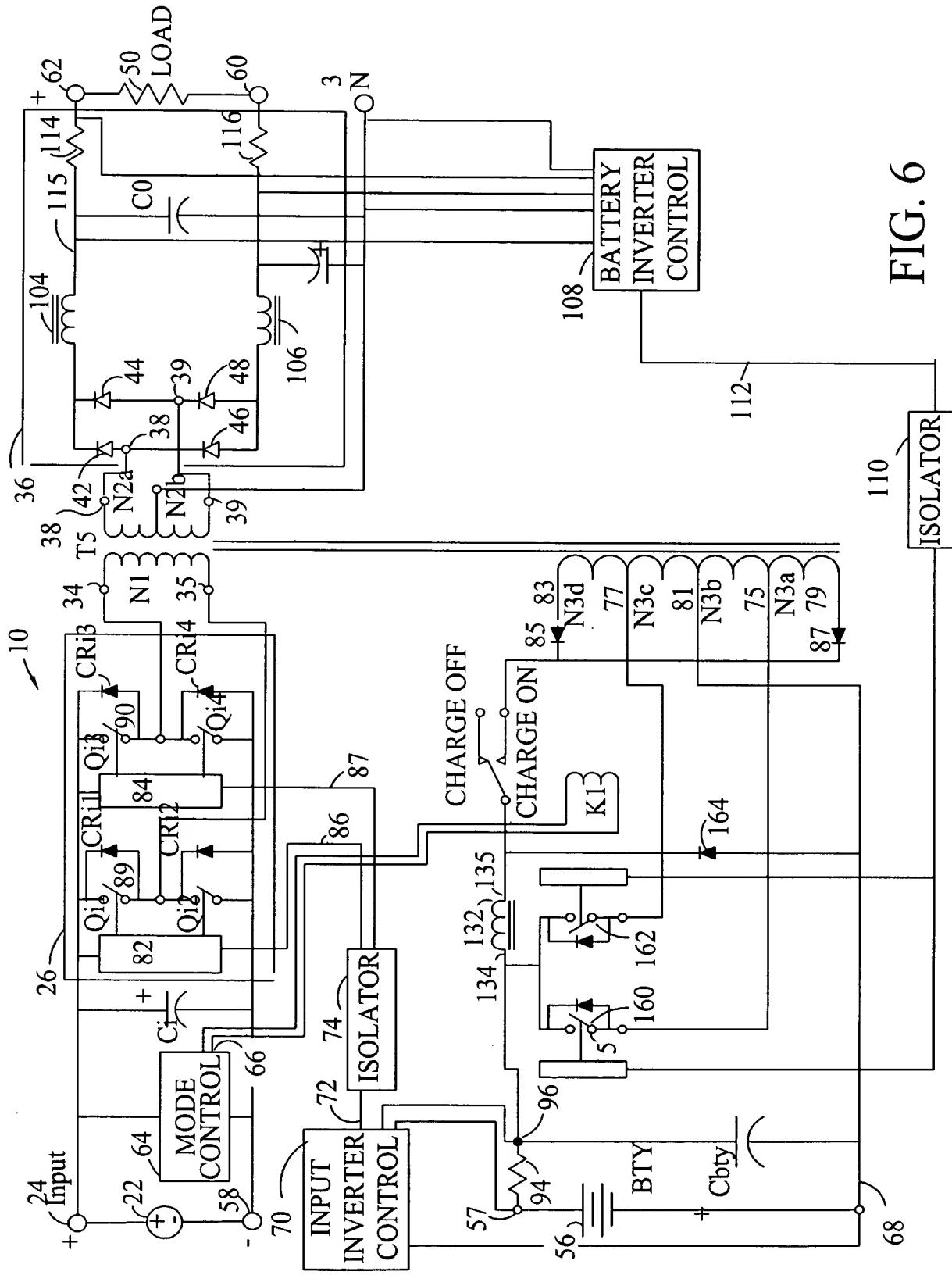


FIG. 6

7/13

STATE
WHILE INPUT PRESENT

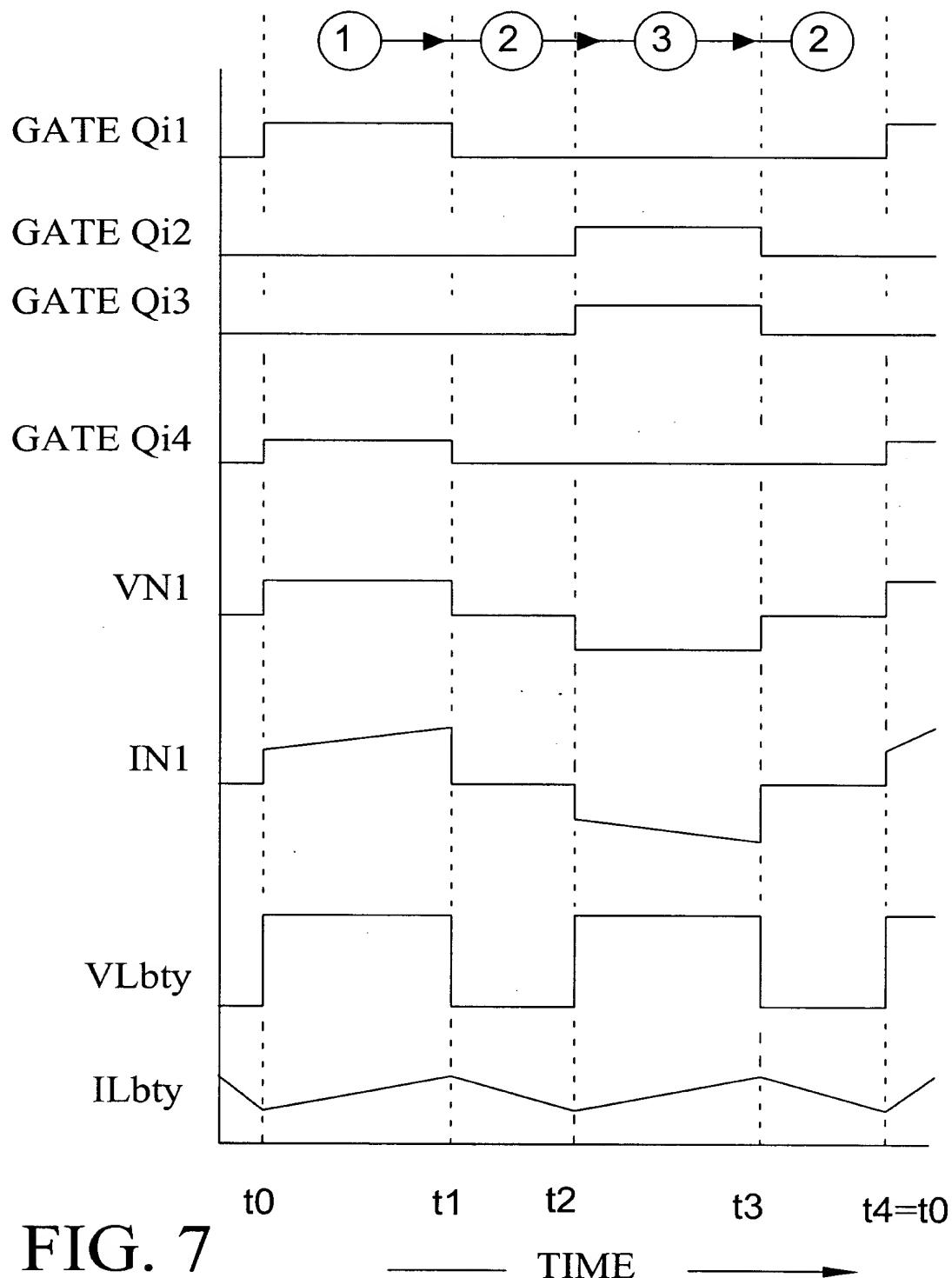


FIG. 7

8/13

	ON	OFF
STATE 1:	1;4	2,3
STATE 2:		1;2;3;4
STATE 3:	2;3	1;4

FIG. 8

9/10

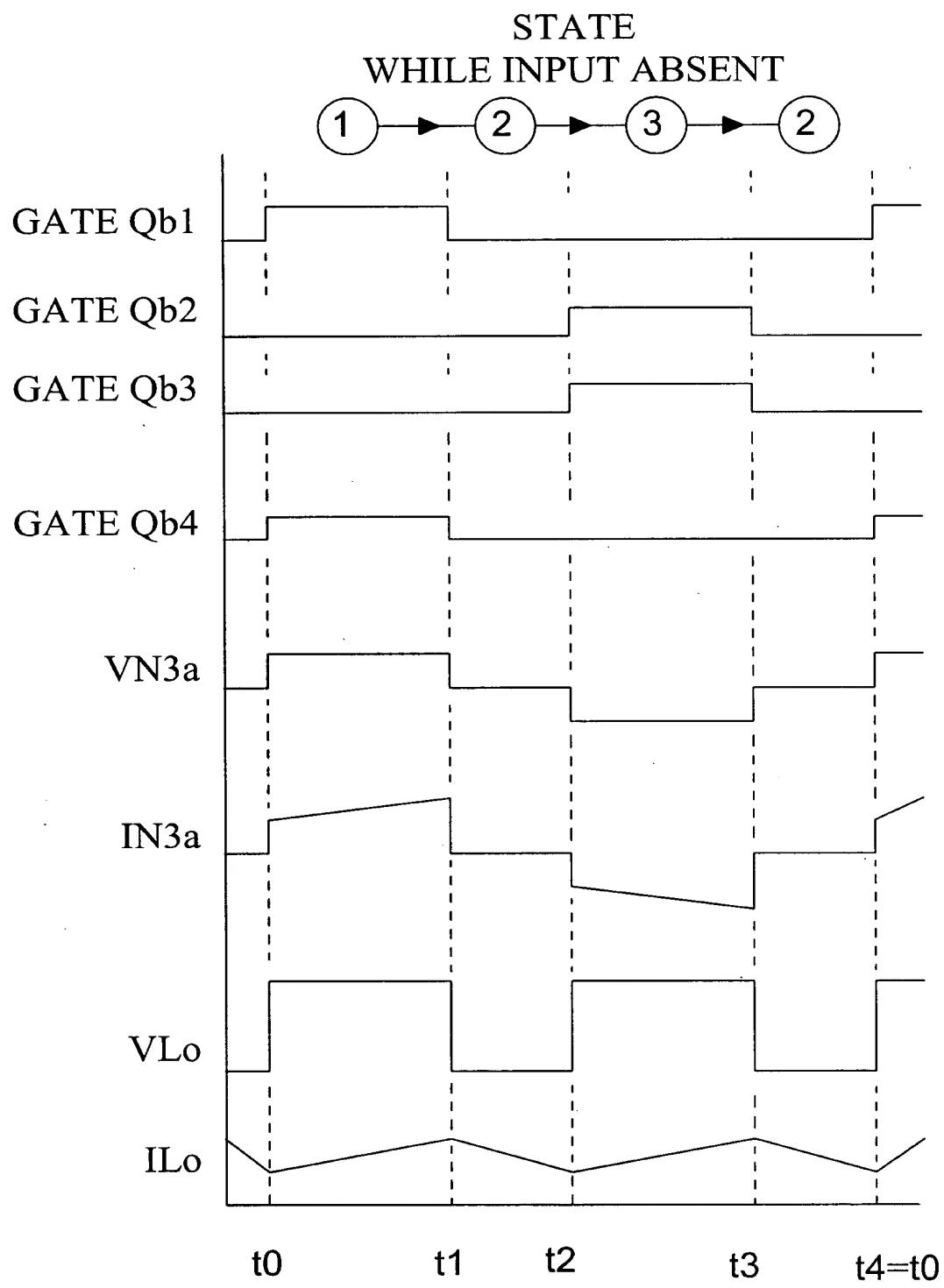
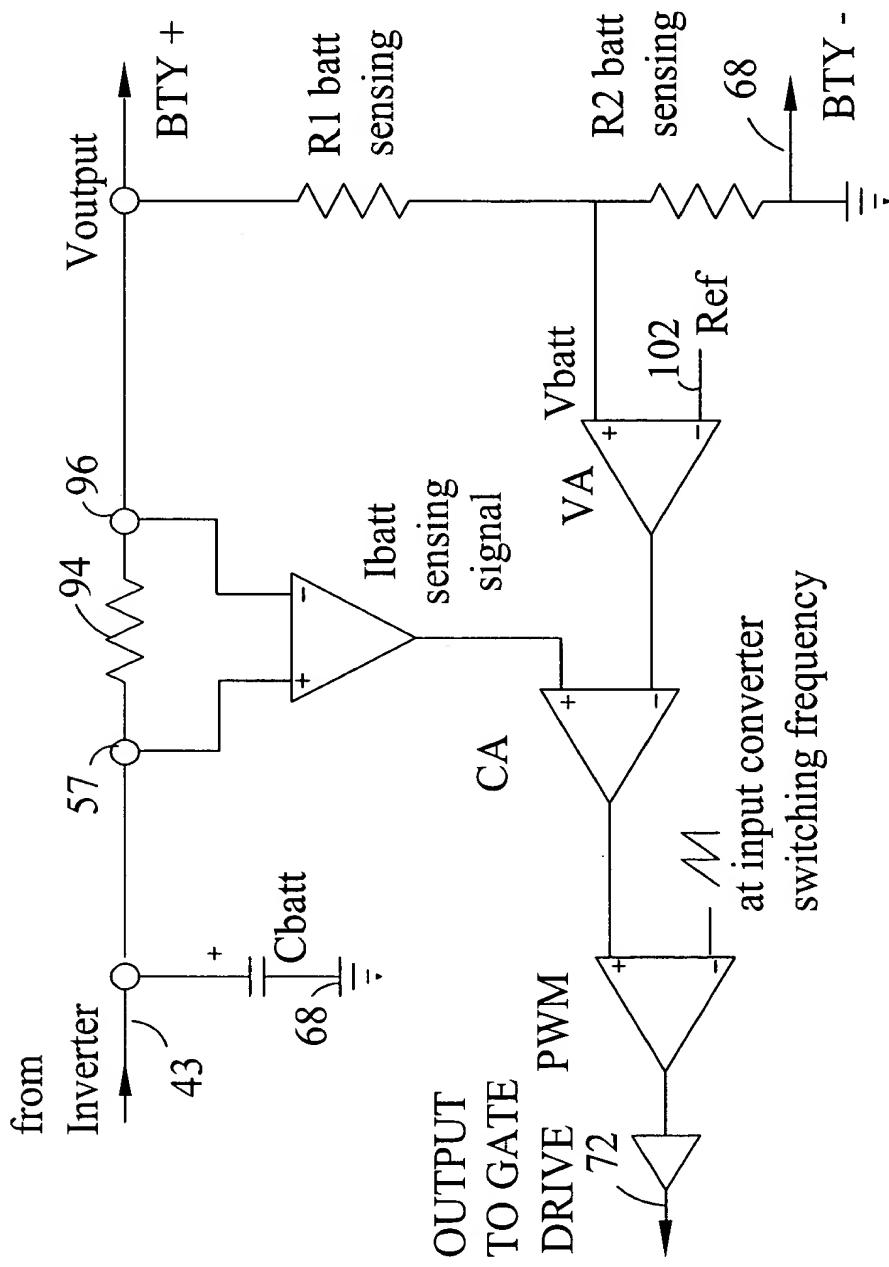
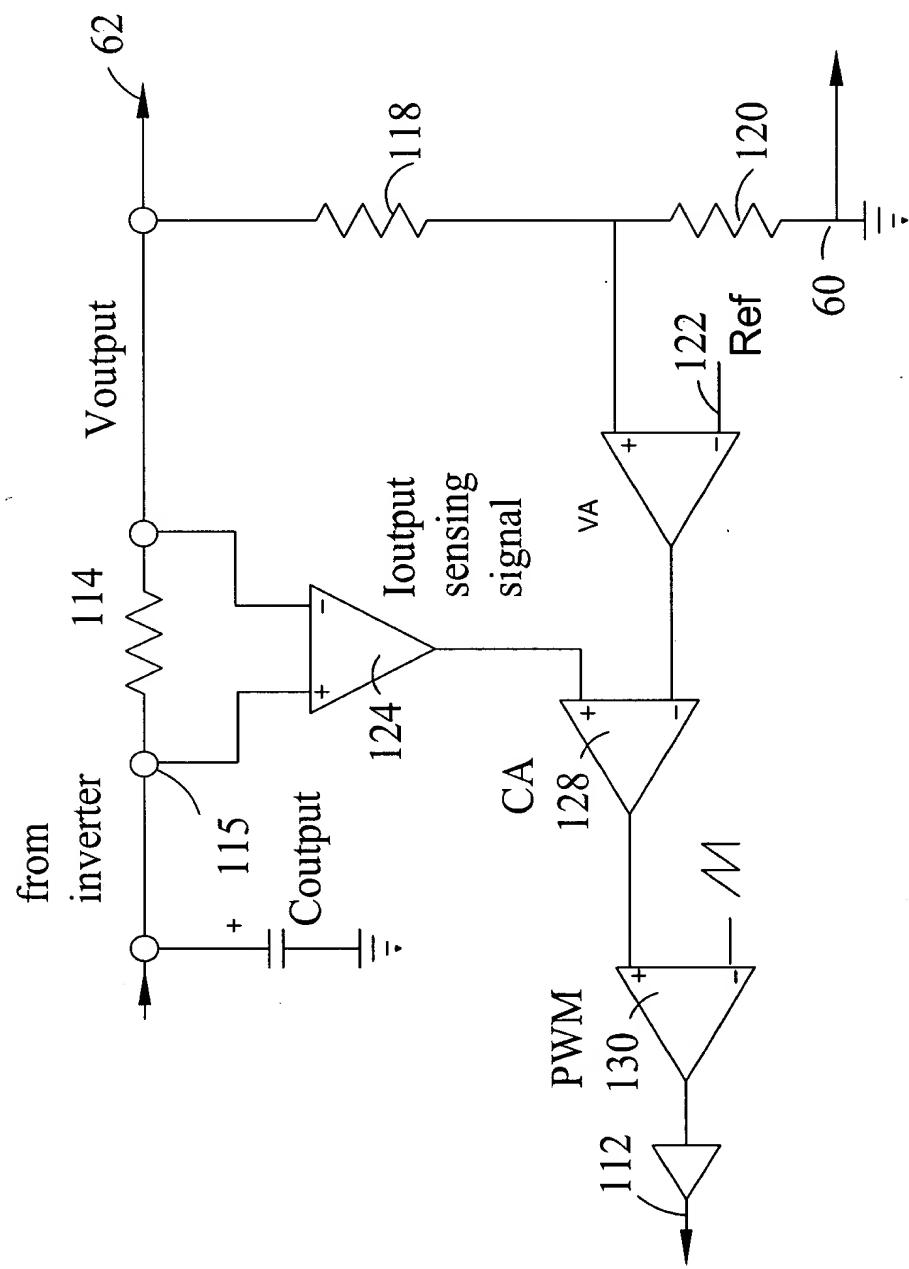


FIG. 9

TIME →



INPUT INVERTER CONTROL SCHEMATIC
FIG. 10



BATTERY INVERTER CONVERTER CONTROL SCHEMATIC

FIG. 11

12/13

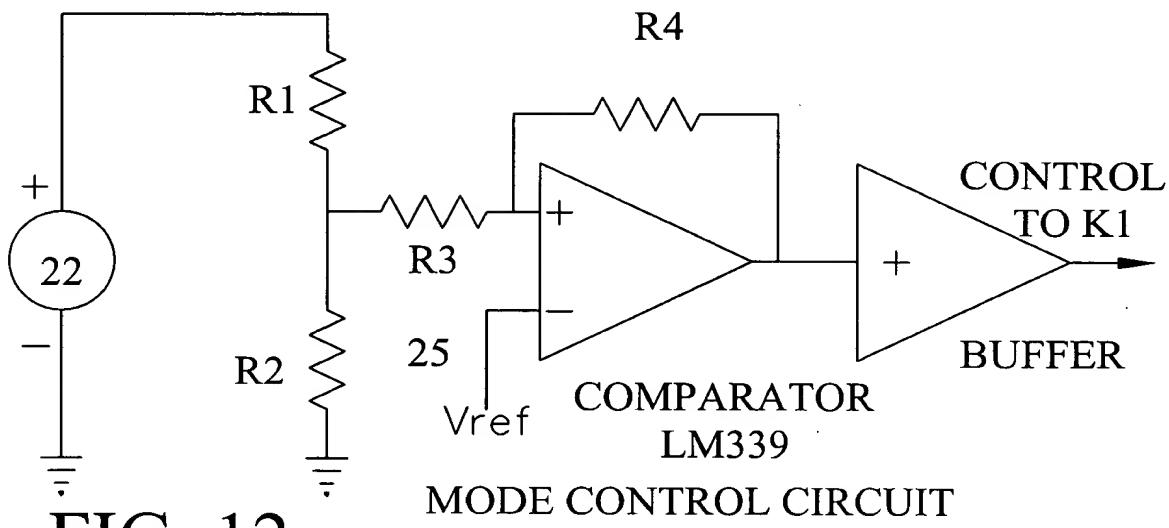


FIG. 12

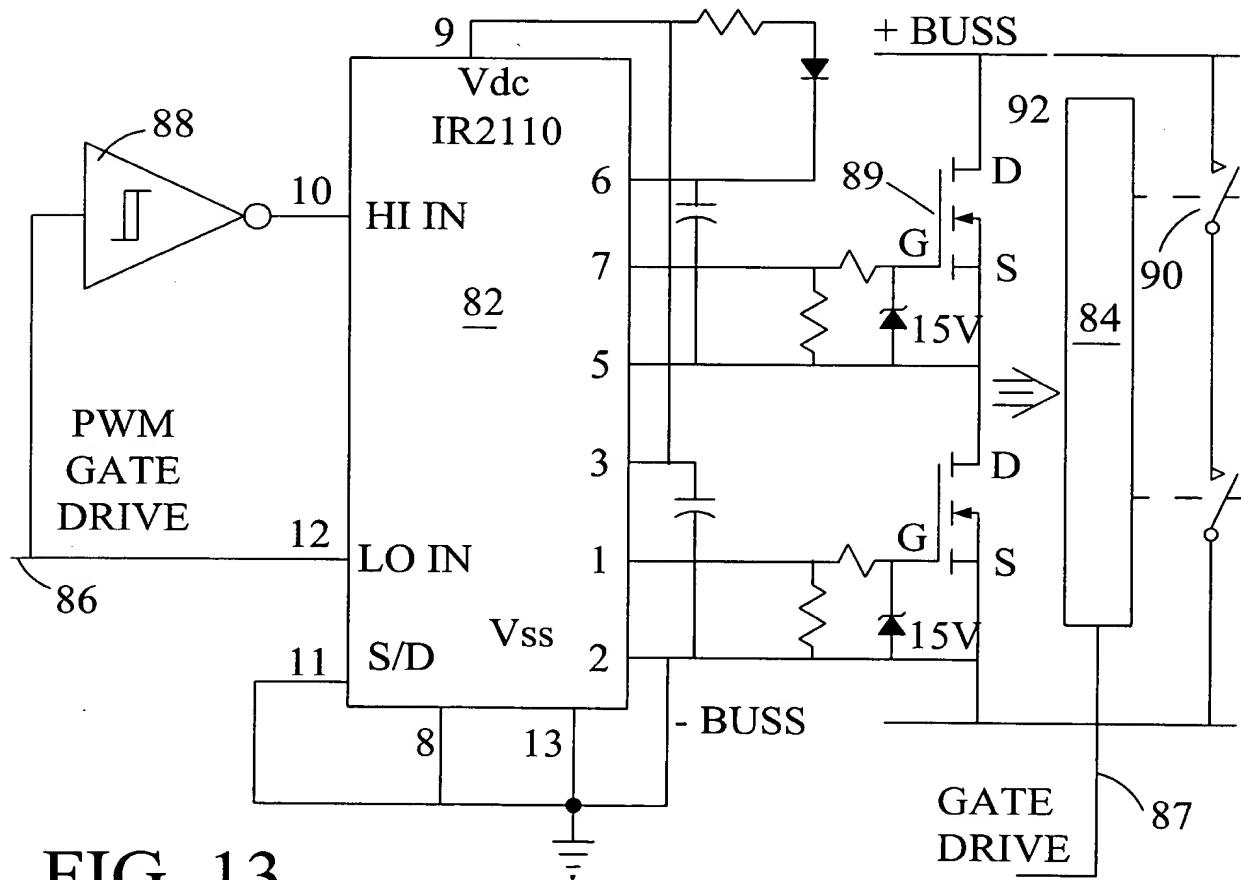


FIG. 13

14/13

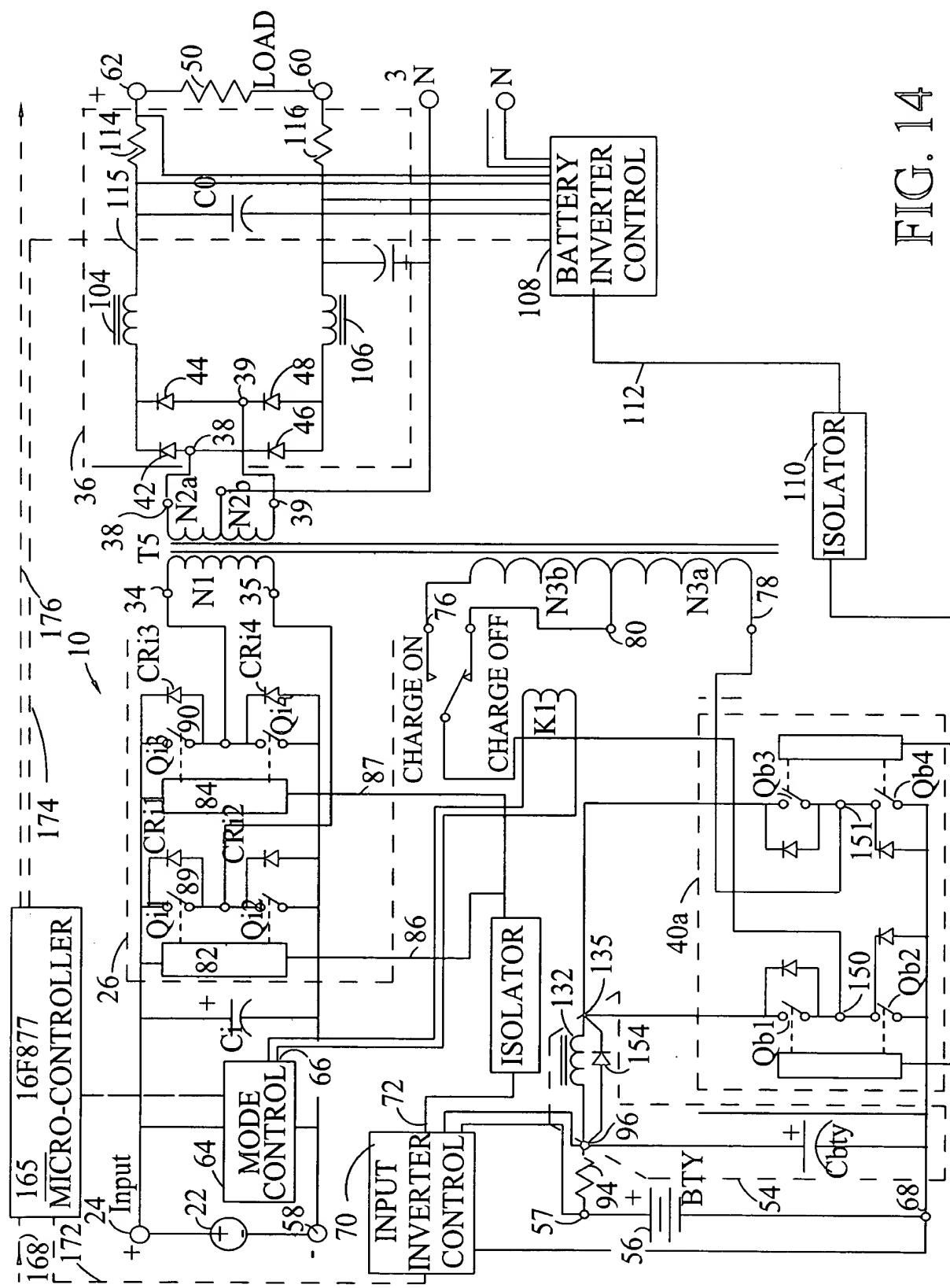


FIG. 14